

Computer I/O Specifications for 275 Solid State Memory

Inputs - All TTL compatible; internal pull-up resistors optional; positive true or ground true selectable.

Value - 9 bits parallel; indicates value of x, y, or z; 8 bits used for x and z, 1 bit of y value is field.

Ident.- 2 bits parallel; identifying value as x,y, or z; 00 causes old values of x, y, and z to be retained.

Read/

Write - 1 bit; specifies operation to be performed.

Latch/

Operate - 1 bit; suppresses read or write operation, allowing new value to be latched and flag to be returned immediately.

Strobe- 1 bit; initiates latch/operate cycle.

Outputs - All TTL compatible with high voltage open collector outputs; internal pull-up resistors optional; positive true or ground true selectable.

Value- 8 bits parallel; contains video output level.

Flag - 1 bit; indicates status of video memory showing cycle complete or data ready.

When a strobe pulse is received, all thirteen input bits are latched in parallel. If no read or write operation is requested, the latch will be completed and the flag returned within one microsecond. If a read or write operation must be performed, the cycle will be completed and the ready status indicated at the beginning of the next horizontal blanking interval of the television raster. This time will vary from one to 64 microseconds.